



Sheet: 1 of: 2

FORM: PTO-1449 (REV: 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	Atty Docket No: 95-0653	Serial No: 08/650,719
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b)) (use several sheets if necessary)		Applicant: Mailloux et al.	
		Filing Date: 5/20/96	Group: 2751

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	
	AA 5,652,724	7/97	Manning	365	189.05	RECEIVED MAR 02 1999 Group 2700
	AB 5,457,659	10/95	Schaefer	365	222	
	AC 5,452,261	9/95	Chung et al.	265	233	
	AD 5,280,594	1/94	Young et al.	395 711	425 157	
	AE 5,058,066	10/91	Yu	365	189.05	
	AF 4,870,622	9/89	Aria et al.	365	230.02	
	AG 5,668,773	09/97	Zagar et al.	365	233	
	AH 5,325,502	06/94	McLaury	395 711	425 169	
	AI 5,373,227	12/94	Keeth	323	313	
	AJ 5,410,670	04/95	Hansen et al.	395 711	425 169	
	AK 5,349,566	09/94	Merritt et al.	365	233.5	

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes	No
	AL					<input type="checkbox"/>	<input type="checkbox"/>
	AM					<input type="checkbox"/>	<input type="checkbox"/>
	AN					<input type="checkbox"/>	<input type="checkbox"/>
	AO					<input type="checkbox"/>	<input type="checkbox"/>
	AP					<input type="checkbox"/>	<input type="checkbox"/>
	AQ					<input type="checkbox"/>	<input type="checkbox"/>

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OTHER ART (including author, title, date, pertinent pages, etc.)

	AR		Micron Technology, Inc., "1995 DRAM Data Book" pp. 4-1 thru 4-42, 12/95
			"Rossini, Pentium, PCI-ISA, Chip Set", Symphony Laboratories, entire book.
	AT		PCT Patent Application No. PCT/US95/16984, filed 12/22/1995, entitled: Burst Edo Memory Device.

Examiner: H. Kim	Date Considered: 11/19/07
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication with applicant.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b)) (use several sheets if necessary)		Applicant: Mailloux et al.	
		Filing Date: 5/28/96	Group: 2751

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass
AA	5,682,354	10/97	Manning	365	233.5
AB	5,640,364	06/97	Merritt et al.	365	233.5
AC	5,729,504	12/95	Cowles	365	236
AD	5,661,695	08/97	Zagar et al.	365	233.5
AE	5,305,284	04/94	Iwase	365	238.5
AF	5,325,330	06/94	Morgan	365	189.05
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FOREIGN PATENT DOCUMENTS

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AO						<input type="checkbox"/>	<input type="checkbox"/>
AP						<input type="checkbox"/>	<input type="checkbox"/>
AQ						<input type="checkbox"/>	<input type="checkbox"/>

Initial

OTHER ART (including author, title, date, pertinent pages, etc.)

		PCT Patent Application No. PCT/US95/16656, filed 12/21/1995, entitled: Burst Edo Memory Device Address Counter.
AS		
AT		

Examiner:

H. Kim

Date Considered:

11/19/07

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